

### **Amendments to the Specification**

Please replace paragraph [0024] using paragraph numbers as in the published application (20030093776) with the following amended paragraph:

[0024] For the computer systems 13 of FIG. 1, source code 8, programmed in a convenient language, represents many application and other programs that collectively constitute a large investment in time and knowledge for owners of native computer systems. The native system 13-1 has available well-perfected compilers/assemblers 9 for forming native executable code 10 [[11]] (legacy code) that efficiently executes application and other programs on the native system 13-1. For the computer systems 13-2, . . . , 13-F, however, well-perfected compilers/assemblers may not be available or, even if available, the source code 8 may not always be available. In order to help preserve the investment in the application and other programs of the native computer system, emulators are employed to execute the executable legacy code on one or more of the target computer systems 13-2, . . . , 13-F. Typically, the target computer systems 13-2, . . . , 13-F are new computer systems that have a different architecture. The objective is to preserve the investment in the application and other programs of the native architecture by enabling them to execute by emulation on the target computer systems.

Please replace paragraph [0026] using paragraph numbers as in the published application (20030093776) with the following amended paragraph:

[0026] In FIG. 2, further details of the host system 16 of FIG. 1 are shown. The group access unit 110 accesses legacy code (LC) and presents the legacy code in groups (LC<sub>G</sub>) to a legacy code translator 21. The legacy code translator 21 stores detailed information about the translation in translation store 24 and flags the loading of a precedent operand for use by a subsequent dependent instruction by setting a translation flag corresponding to that precedent operand in register flag store 25. The legacy code translator 21 also stores the executable blocks of host code in a translated code (TC) cache 23. The translated code (TC<sub>F</sub>) output from the cache 23 is executed in execution unit 13-F.

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Please replace Abstract with the following amended Abstract:

Emulation of a guest computer architecture on a host system of another computer architecture. [[The guest computer architecture has programs composed of legacy instructions. Each particular legacy]] Legacy instructions [[is]] are translated into [[one or more particular]] translated instructions [[for emulating the particular legacy instruction]]. If the particular legacy instruction is an operand-setting instruction for storing a value of a precedent operand, a corresponding flag is set when the value of the precedent operand has not been determined. If the particular legacy instruction is an operand-using instruction for using the precedent operand, a check is made to determine if the corresponding flag is set. [[If the corresponding flag is set, translation of the operand-using instruction is suspended and the one or more particular translated instructions corresponding to the operand-setting instruction are executed to determine the value of the precedent operand. Thereafter, the translation of the legacy instructions is resumed using the value of the precedent operand in the resumed translation. If the corresponding flag is not set, the translation of the operand-using instruction continues without suspension.]]